SuperComputing 2016

Salt Palace Convention Center, Salt Lake City, Utah
(November 12-18 2016)

Stop by the OSC/OH-TECH booth (#1107) during show hours to view presentations/demos on various topics related to the MVAPICH and HiBD projects.
Also, talk in person with the project team members!

Saturday, November 12

1:30PM - 2:00PM   HP-CAST, Mariott Hotel Downtown City Creek
MPI+X - The Right Programming Paradigm for Exascale?
[Talk]
D. K. Panda

3:10PM - 4:00PM   Intel HPC Dev Conf, Sheraton, Room - Sundance
High-Performance and Scalable MPI+X Library for Emerging HPC Clusters
[Talk]
D. K. Panda, K. Hamidouche

Sunday, November 13

9:45AM - 10:35AM   Intel HPC Dev Conf, Sheraton, Room - Deer Valley
Exploiting HPC Technologies to Accelerate Big Data Processing (Hadoop, Spark, and Memcached)
[Talk]
D. K. Panda, X. Lu

1:30PM - 5:00PM   #355-C
Big Data Meets HPC: Exploiting HPC Technologies for Accelerating Apache Hadoop, Spark, and Memcached
[Tutorial]
D. K. Panda, X. Lu

Monday, November 14

8:30AM - 12:00PM   #355-C
InfiniBand and High-Speed Ethernet for Dummies
[Tutorial]
D. K. Panda, H. Subramoni

1:30PM - 2:00PM   PDSW-DISCS Workshop, #155-C
Can Non-Volatile Memory Benefit MapReduce Applications on HPC Clusters?
[Technical Paper]
M. W. Rahman

3:50PM - 4:10PM   PAW Workshop, #251-B

OpenSHMEM NonBlocking Data Movement Operations with MVAPICH2-X: Early Experiences
[Technical Paper]
K. Hamidouche

Tuesday, November 15

10:45AM - 11:45AM   NRL Booth #3453
MVAPICH2 MPI Libraries to Exploit Latest Networking and Accelerator Technologies
[Talk]
D. K. Panda

1:52PM - 2:14PM   #155-C
DS6. High Performance File System and I/O Middleware Design for Big Data on HPC Clusters
[Doctoral Showcase]
N. Islam

2:00PM - 4:00PM   PGAS Booth #537
MVAPICH2-X: Unified Communication Runtime for Efficient Hybrid MPI + PGAS Programming Models
[Poster Presentation]
K. Hamidouche

2:30PM - 3:00PM   Mellanox Theater, Booth #2631
The MVAPICH2 Project: Latest Developments and Plans Towards Exascale Computing
[Talk]
D. K. Panda

5:15PM - 7:00PM   Exhibit Hall E, Booth #122
DS6. High Performance File System and I/O Middleware Design for Big Data on HPC Clusters
[Doctoral Showcase]
N. Islam

5:15PM - 7:00PM   Lower Lobby Concourse, Exhibit Hall E - Booth #104
SRC07. Job Startup at Exascale: Challenges and Solutions
[Poster Presentation]
S. Chakraborty

Wednesday, November 16

11:00AM - 11:30AM   #355-D
Designing MPI Library with On-Demand Paging (ODP) of InfiniBand: Challenges and Benefits
[Technical Paper]
M. Li

12:15PM - 1:15PM   PGAS Bof #355-E
MVAPICH2-X: High-Performance MPI and PGAS Libraries on Modern Clusters
[Lighting Talk]
K. Hamidouche
1:00PM - 1:30PM   NVIDIA GPU Technology Theater, Booth #2217
MVAPICH2-GDR: Pushing the Frontier of HPC and Deep Learning

[Talk]
D. K. Panda

2:00PM - 4:00PM   PGAS Booth #537
Co-Designing and Acceleration HPC Applications with a Hybrid MPI + PGAS Models
[Poster Presentation]
M. Li

5:30PM - 6:00PM   HPC Connection Workshop, Grand America Hotel, Room - Riviera
Designing HPC, Big Data, and Deep Learning Middleware for Exascale Systems: Challenges and
Opportunities
[Talk]
D. K. Panda

Thursday, November 17

11:00AM - 11:30AM   Mellanox Theater, Booth #2631
MVAPICH2-GDR: Pushing the Frontier of HPC and Deep Learning
[Talk]
D. K. Panda

Friday, November 18

8:45AM - 12:40PM   #155-E
ESPM2 2016: Second International Workshop on Extreme Scale Programming Models and Middleware
[Workshop]
K. Hamidouche, H. Subramoni, D. K. Panda

9:15AM - 9:35AM   COMHPC Workshop, #355-D
Efficient Reliability Support for Hardware Multicast-Based Broadcast in GPU-Enabled Streaming
Applications
[Technical Paper]
C. Chu